

Remaining material in this course:

Shur, Chapter 4, Sections:

Sect. 4.1-4.5

Sect. 4.7

Sect. 4.9

Sect. 4.11

Shur, Chapter 7, all sections

Required Problems:

4-2-6

4-3-3

4-4-1

4-4-3

4-4-6

4-5-7

4-5-10

4-7-2

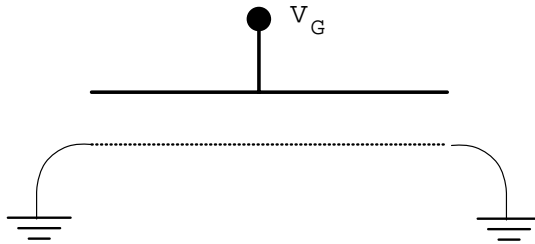
4-11-1†

7-3-1

Shall we have a class on November 25 ?

† Note a misprint in the formulation of this problem; it is Eq. (4-11-7) that must be derived, not Eq. (4.11.12).

Two Dimensional Channel



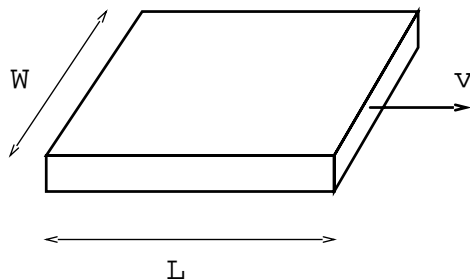
$$e n [\text{cm}^{-2}] = \frac{C}{A} V_G$$

Conductance of 3D sample:

$$g \equiv \frac{dI}{dV} = [e N \mu] \frac{A}{L}, \quad e N \mu \left[\frac{1}{\Omega \cdot \text{cm}} \right]$$

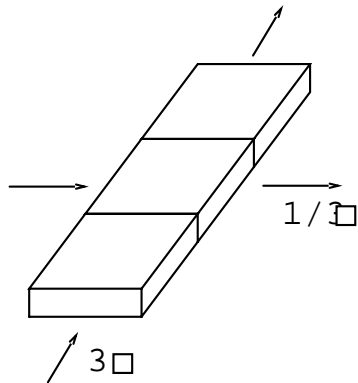
Conductance of 2D sample:

$$g \equiv \frac{dI}{dV} = [e n \mu] \frac{W}{L}, \quad e n \mu \left[\frac{1}{\Omega} \right]$$



Note: resistance of a square independent of its size (contrast with a cube!) $\rightarrow \Omega \square$

Knowing the "resistance per square" $\frac{1}{en\mu}$
one can simply count squares:



Current density per unit width [A cm^{-1}]

$$J \equiv \frac{I}{W} = en v$$

$$= en \mu F = en \mu \frac{V}{L}$$

conductance per unit channel width $\leftarrow g = en \mu/L$ [mS/mm]

Transconductance (also per unit width)

$$g_m \equiv \left. \frac{\partial J}{\partial V_G} \right]_{V_D}$$

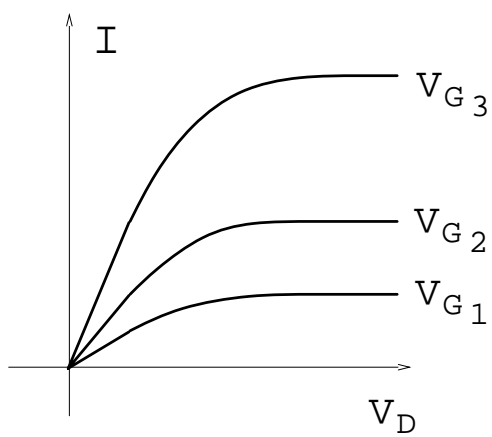
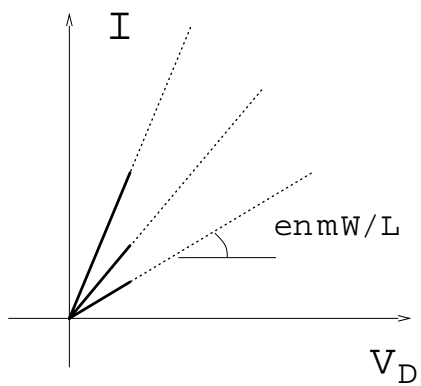
$$J = en v$$

$$g_m = \frac{\partial(en)}{\partial V_G} v = \frac{C}{A} v$$
 [mS/mm]

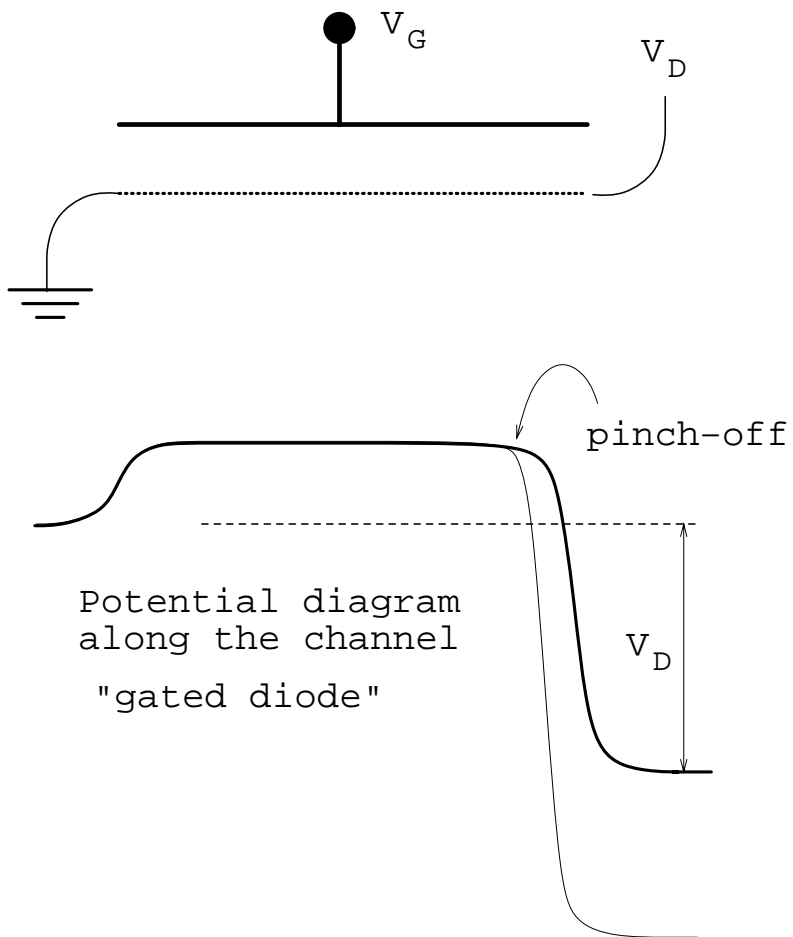
Figure of Merit ("FOM"):

$$\frac{C}{g_m} = \frac{L}{v} \quad (\text{delay time})$$

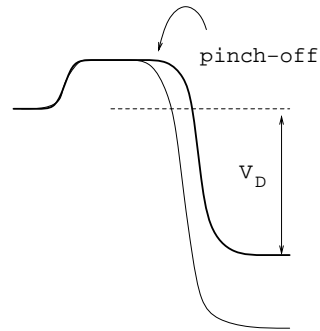
Characteristics



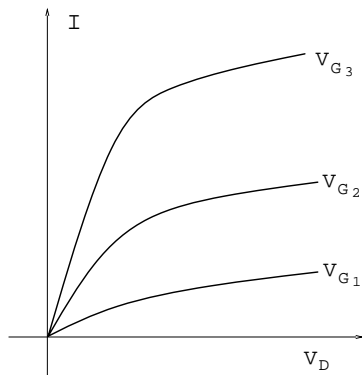
Transistor channel
versus thin film diode



Short channel effects



As the pinch off point moves left, the channel becomes shorter.
The decreasing L leads to increasing current (finite output conductance)
Recall Early effect in bipolars

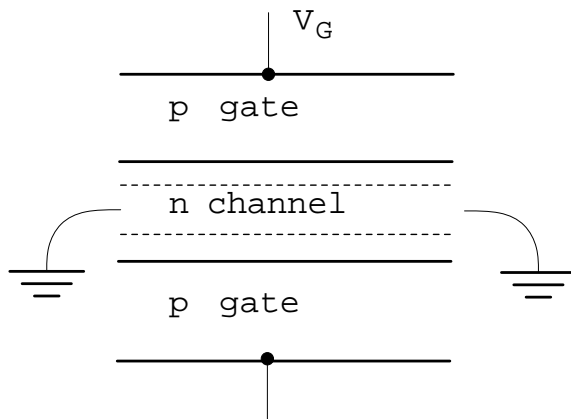


For a given channel length, the closer the gate to the channel, the less important are the short-channel effects

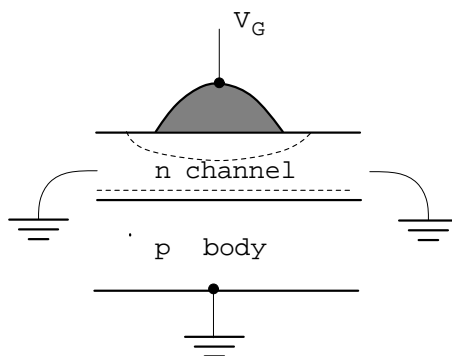
JFET and MESFET

Channel is an undepleted portion of a thin layer
Thickness of the undepleted portion is controlled by a gate

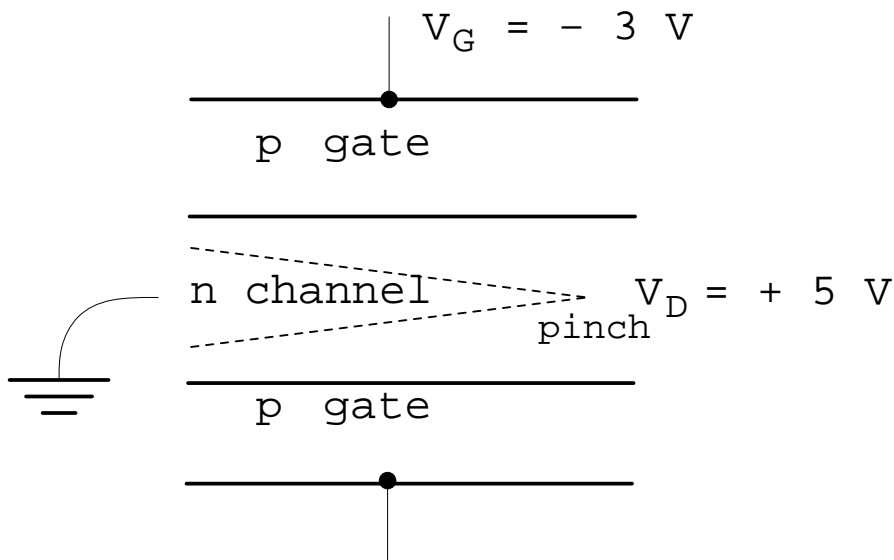
Junction FET



Metal-Semiconductor FET



Pinch off in a JFET



Gradual channel approximation

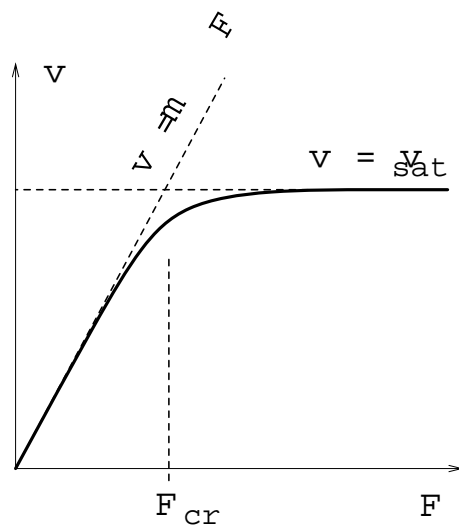
Treat the potential diagram at any channel cross-section (x) as if there is no current, no voltage difference between the source and the drain, but the channel is not at ground voltage but at $V = V_{ch}(x)$

This is equivalent to replacing $V_G \rightarrow V_G - V_{ch}(x)$

For a sufficiently high V_D the effective value of the gate voltage goes below threshold near the drain and pinch off forms. When that happens, the characteristics saturate because the pinch-off point does not move very much with increasing V_D (think why!).

In short channel devices, the small decrease of the effective channel length is still appreciable and characteristics do not saturate.

Velocity-field characteristics



Typical numbers to remember:

$$v_{sat} \approx 10^7 \text{ cm/s}$$

$$n \approx 10^{12} \text{ cm}^{-2}$$

$$J = e n v_{sat} \approx 1 \text{ A/cm} = 100 \text{ mA/mm}$$

MOS structure

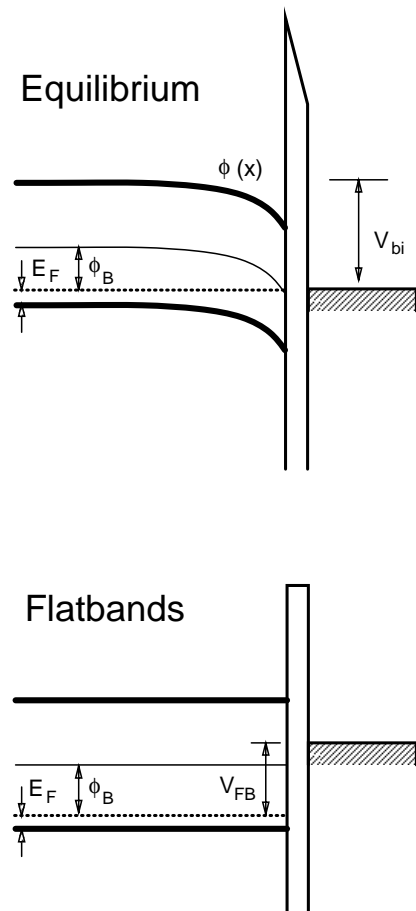


Figure 1: Built-in voltage V_{bi} and flatband voltage V_{FB} .

These quantities are not identical.

V_{bi} is the electrostatic potential drop in equilibrium: it equals sum of the potential drops in the semiconductor and the oxide.

V_{FB} is the voltage that must be applied to the gate to flatten the bands. Since "voltage" means the Fermi level difference,[†] the V_{FB} equals the difference in the work functions of the semiconductor and the metal.

[†] Not the same thing as the electrostatic potential difference, which can exist even in equilibrium.

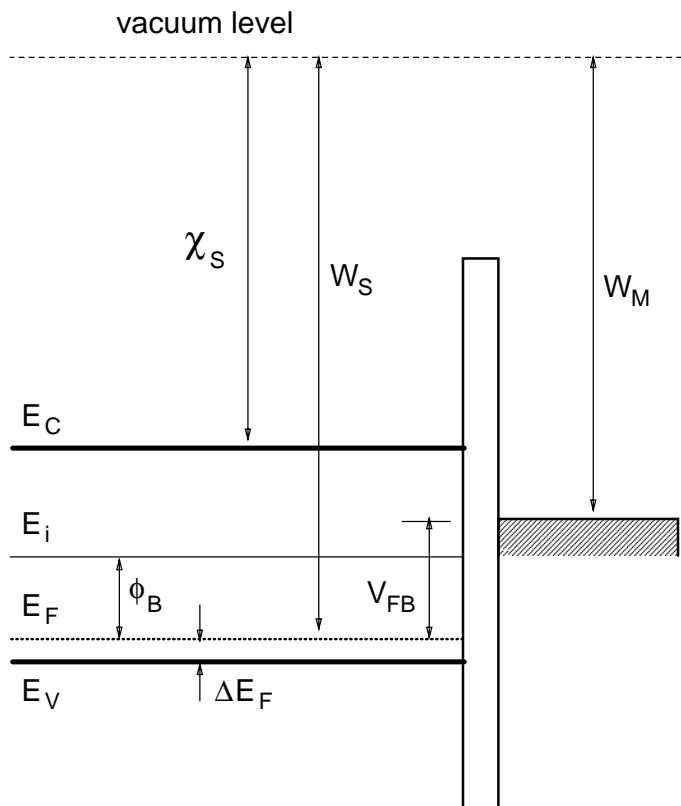


Figure 2: Relevant energies in the MOS system at flatbands.

- W_S : work function of the semiconductor
- W_M : work function of the metal
- $V_{FB} = W_S - W_M$: flatband gate voltage
- χ_S : electron affinity of the semiconductor
- $\phi_B \equiv E_i - E_F$: bulk doping characteristic, $\phi_B = kT \ln(N_A/n_i)$.

$$\begin{aligned}
 W_S &= \chi_S + E_G - \Delta E_F \\
 &= \chi_S + (E_C - E_V) - (E_F - E_V) \\
 &= \chi_S + E_C - E_F
 \end{aligned}$$

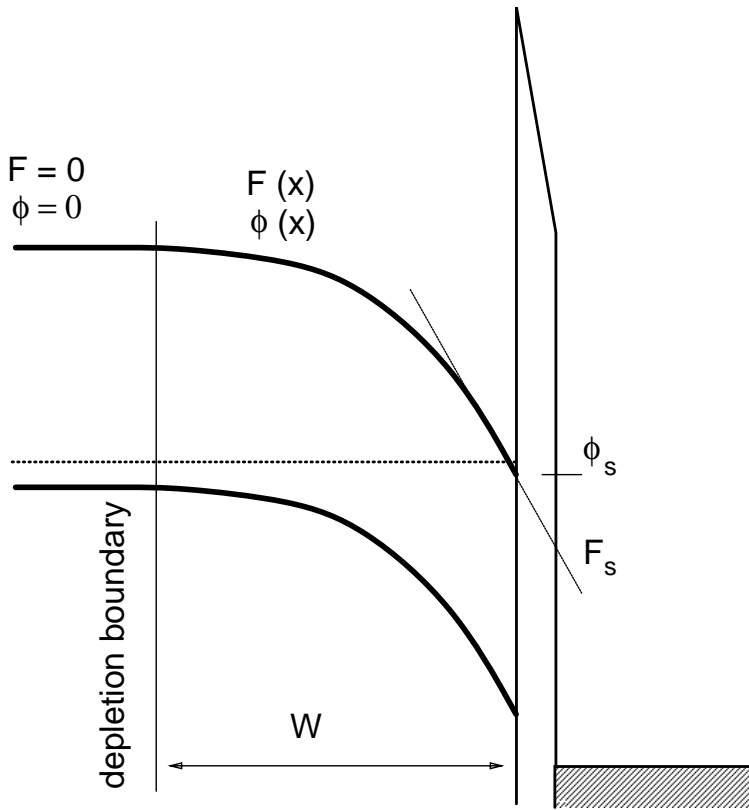


Figure 3: Evaluation of the band bending in MOS structure

Poisson's equation:
$$\phi'' = -\frac{e(N_A - p)}{\epsilon}$$

$p = N_A e^{-\beta\phi}$ \rightarrow
$$\phi'' = -\frac{eN_A}{\epsilon} \left[1 - e^{-\beta\phi} \right] \quad (1)$$

Note a trick:
$$\phi'' \equiv \frac{d\phi'}{dx} = \frac{d\phi'}{d\phi} \frac{d\phi}{dx} = \frac{dF}{d\phi} F = \frac{1}{2} \frac{dF^2}{d\phi}$$

\rightarrow multiply Poisson's equation (1) by $d\phi$ and integrate

from	$x = -\infty$	$\phi = 0$	$F = 0$
to	$x = \text{surface}$	$\phi = \phi_s$	$F = F_s$

using

$$\int_0^{\phi_s} \left[1 - e^{-\beta\phi} \right] d\phi = \frac{kT}{e} \left[\beta\phi_s + e^{-\beta\phi_s} - 1 \right]$$

Exact:

$$\frac{1}{2} F_S^2 = \frac{kT N_A}{\epsilon} \left[\beta \phi_S + e^{-\beta \phi_S} - 1 \right]$$

Approximate ($\beta \phi_S \gg 1$, i.e., $\phi_S \gg kT/e$):

$$F_S^2 = \frac{2kT N_A}{\epsilon} \beta \phi_S = \frac{2e N_A}{\epsilon} \phi_S$$

The same result is obtained in the depletion approximation:

$$\phi_S = \frac{e N_A W^2}{2 \epsilon} \quad F_S = \frac{e N_A W}{\epsilon} \quad \text{where } W \text{ is the depletion width}$$

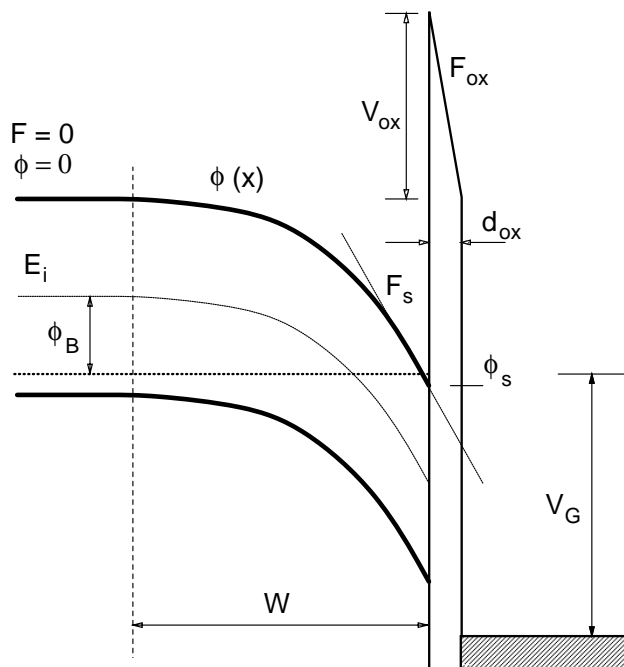


Figure 4: Threshold gate voltage evaluation:

Given bulk doping N_A

→ $e \phi_B = kT \ln(N_A/n_i)$

at threshold $\phi_S = 2 \phi_B$

→ $F_S = \sqrt{\frac{4e N_A \phi_B}{\epsilon_S}}$

$\epsilon_S F_S = \epsilon_{OX} F_{OX}$

→ $F_{OX} = \frac{\epsilon_S}{\epsilon_{OX}} \sqrt{\frac{4e N_A \phi_B}{\epsilon_S}}$

$V_{OX} \equiv d_{OX} F_{OX}$

→ $V_T = e \phi_S + V_{OX} + V_{FB}$

Pinch off in a MOSFET

Recall the gradual channel approximation: Treat the potential diagram *locally* at any channel cross-section (x) by ignoring the voltage difference between the source and the drain, but taking the channel to be not at ground voltage but at $V = V_{ch}(x)$, i.e., by replacing

☛ $V_G \rightarrow V_G - V_{ch}(x)$

The $V_{ch}(x)$ is the imref (the quasi-Fermi level) which varies monotonically from $E_F = 0$ in the source to $E_F = V_D$ in the drain.

