# **Ebers-Moll model**



**Figure 1**: Dashed line indicates the "intrinsic" portion of the device, excluding "parasitic" extrinsic elements.

The model for the diode blocks can be further specified to include the internal junction capacitance.



## Small-signal equivalent circuit of an abrupt junction HBT

**Figure 2**: This model is good for "ballistic" propagation of carriers across the base. For diffusive propagation, the intrinsic portion must be adjusted,<sup>†</sup>

<sup>†</sup> A. A. Grinberg and S. Luryi, IEEE Trans. Electron Devices ED-40, pp. 1512-1522 (1993).

# **Elements of small-signal analysis**

All variables *A*(*t*) are considered varying *harmonically* in a small range about a dc point:

$$A(t) = A_0 + \delta A e^{i\omega t}$$
$$I(t) = I_0 + \delta I e^{i\omega t}$$
$$V(t) = V_0 + \delta V e^{i\omega t}$$

Many alternative notations, e.g., *i* and *v* instead of  $\delta I$  and  $\delta V$ .

The  $\delta A$  's are *complex* quantitites, may be position-dependent fields,  $\delta A(\vec{x})$ .

The relationship between different  $\delta A$ 's, e.g. between  $\delta V$  and  $\delta I$  (generalized impedances or admittances) depend on the chosen dc point.



**Figure 3**: General two-port. Transformers are 2-ports ("passive"). From the small-signal point of view, transistors are two-port amplifiers.

Admittance matrix:

$$\begin{bmatrix} \delta I_1 \\ \delta I_2 \end{bmatrix} = \begin{bmatrix} y_{11} & y_{12} \\ y_{21} & y_{22} \end{bmatrix} \begin{bmatrix} \delta V_1 \\ \delta V_2 \end{bmatrix}$$

#### Admittance matrix:

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For example:

$$y_{11} \equiv \left[\frac{\mathrm{d}I_1}{\mathrm{d}V_1}\right]_{V_2}$$
, input admittance

Impedance matrix:

$$\begin{bmatrix} \delta V_1 \\ \delta V_2 \end{bmatrix} = \begin{bmatrix} z_{11} & z_{12} \\ z_{21} & z_{22} \end{bmatrix} \begin{bmatrix} \delta I_1 \\ \delta I_2 \end{bmatrix}$$

For example:

$$z_{22} \equiv \left[ \frac{\mathrm{d} V_2}{\mathrm{d} I_2} \right]_{I_1}$$
, output impedance

Hybrid matrix (h-parameters):

$$\begin{bmatrix} \delta V_1 \\ \delta I_2 \end{bmatrix} = \begin{bmatrix} h_{11} & h_{12} \\ h_{21} & h_{22} \end{bmatrix} \begin{bmatrix} \delta I_1 \\ \delta V_2 \end{bmatrix}$$

For example:

$$h_{21} \equiv \left[ \frac{\mathrm{d} I_2}{\mathrm{d} I_1} \right]_{V_2}$$
, forward current gain

Definition of these parameters essentially involves specification of the **boundary condition** at one or another port. Thus

 $h_{22}$  is the output admittance for **open-circuit** input port.

 $y_{22}$  is the output admittance for *short-circuit* input port.

 $h_{21}$  is the forward current gain for **short-circuit** output port, etc.

Each set of parameters (*z*-parameters, *y*-parameters, *h*-parameters) is *complete* in the sense that it can be used to derive the other sets unambiguously.

# **Common terminal configurations**



**Figure 4**: Different common-terminal configurations give rise to very different parameters. Thus, the short-circuit current gains are

$$h_{21}^{e} \equiv \beta$$
  $h_{21}^{b} \equiv \alpha$ 

and hence

$$h_{21}^{\rm e} = \frac{1-h_{21}^{\rm b}}{h_{21}^{\rm b}}$$

#### **Indefinite parameters**

All of the parameter sets corresponding to different configurations (commonbase, common-emitter, common-collector) are derivable from one another.

A convenient trick (works best in *y*-parameter representation) is to disregard the common reference and treat the third terminal as an additional port:



Figure 5: indefinite matrix:

$\int \delta I_1$		y <sub>11</sub>	<i>Y</i> <sub>12</sub>	<i>y</i> <sub>13</sub>	$\int \delta V_1$	]
$\delta I_2$	=	<i>y</i> <sub>21</sub>	$y_{22}$	<i>Y</i> 23	$\delta V_2$	
$\delta I_3$		<i>y</i> <sub>31</sub>	$y_{32}$	<i>y</i> <sub>33</sub>	$\delta V_3$	

From Kirhhoff's Law and the fact that the matrix should work for *arbitrary* set of  $\{\delta V_i\}$  it follows that the sum of all columns (or rows) in the indefinite matrix is zero.

Thus, if we assume a short circuit at ports 1 and 2, the fact that the sum of all currents must be zero implies that the  $\sum$  of *y*-parameters in the *third* column vanishes, and so on.

To prove that the  $\sum$  must vanish in each *row*, we note that if all three { $\delta V_i$ } are equal no ac current can flow at any port.

It is exceedingly simple to transform from one common-terminal configuration to another. Thus, if we know the *y*-matrix in common base configuration, the corresponding common-emitter matrix is:

$\mathbf{y}_{11}^{\mathrm{b}}$	$\mathbf{y}_{12}^{\mathrm{b}}$	<i>y</i> <sub>13</sub>		<b>y</b> <sub>11</sub>	<i>Y</i> <sub>12</sub>	<i>y</i> <sub>13</sub>		<b>y</b> <sub>11</sub>	<i>Y</i> 12	<i>y</i> <sub>13</sub>
$\mathbf{y}_{21}^{\mathrm{b}}$	$\mathbf{y}_{22}^{\mathrm{b}}$	<i>Y</i> <sub>23</sub>	$\rightarrow$	<i>Y</i> 21	<i>Y</i> <sub>22</sub>	<i>Y</i> <sub>23</sub>	$\rightarrow$	y <sub>21</sub>	$\mathbf{y}_{11}^{\mathrm{e}}$	<b>y</b> <sub>12</sub> <sup>e</sup>
y <sub>31</sub>	<i>y</i> <sub>32</sub>	<i>y</i> <sub>33</sub>		y <sub>31</sub>	<i>y</i> <sub>32</sub>	<i>y</i> <sub>33</sub>		y <sub>31</sub>	$\mathbf{y}_{21}^{\mathrm{e}}$	<b>y</b> <sup>e</sup> <sub>22</sub>

#### Power gain definitions:

• *Power gain G* is the ratio of power delivered to the load to power input into the network.

It depends on both the input and the load circuits.

• *Maximum available gain* (MAG) is the maximum gain achievable from a particular transistor without external feedback.

MAG equals the value of forward gain *G* which results when both the input and the output are simultaneously *matched* in an optimum way. For example, realization of MAG requires that the load resistance be matched to the output resistance  $Re(z_{22})$ .

• **Unilateral gain** *U* is the maximum available power gain of a device after it has been made unilateral by adding a lossless reciprocal feedback circuit. This means that the lossless network around the amplifier (inductances and capacitances) is adjusted so as to set the reverse power gain to zero.

Unilateral gain is *independent*<sup>†</sup> of common-lead configuration !

The unilateral gain U can be calculated from any of the following equivalent expressions:

$$U = \frac{|z_{21} - z_{12}|^2}{4 [Re(z_{11}) Re(z_{22}) - Re(z_{12}) Re(z_{21})]};$$
  
=  $\frac{|y_{21} - y_{12}|^2}{4 [Re(y_{11}) Re(y_{22}) - Re(y_{12}) Re(y_{21})]};$   
=  $\frac{|h_{21} + h_{12}|^2}{4 [Re(h_{11}) Re(h_{22}) + Im(h_{12}) Im(h_{21})]},$ 

where  $z_{ij}$ ,  $y_{ij}$ , and  $h_{ij}$  are the impedance, the admittance, and the hybrid parameters of a transistor, respectively, *for any configuration*.

<sup>&</sup>lt;sup>†</sup> This remarkable result (Mason's theorem) is the main reason for the wide-spread use of *U*. See S. J. Mason, "Power gain in feedback amplifiers", *IRE Trans. Circuit Theory* **CT-1**, pp. 20-25 (1954).



#### Small-signal model of an abrupt junction HBT

**Figure 6**: Small-signal analysis of this simple model, including frequency dependence of the power gain in both ballistic and diffusive regimes, has been carried out by Grinberg and Luryi (1993).<sup>†</sup>

<sup>†</sup> A. A. Grinberg and S. Luryi, "Coherent transistor", *IEEE Trans. Electron Devices* ED-40, pp. 1512-1522 (1993).

A. A. Grinberg and S. Luryi, "Dynamic Early effect in heterojunction bipolar transistors", *IEEE Electron Device Lett.* EDL-14, pp. 292-294 (1993).

Quasi-static (Ebers-Moll-like) model of abrupt-junction HBT can be found in

A. A. Grinberg and S. Luryi, "On the thermionic-diffusion theory of minority transport in heterostructure bipolar transistors", *IEEE Trans. Electron Devices* **ED-40**, pp. 859-866 (1993).





Example: partial coherence



Coherent transistor is limited by the dispersion  $\Delta\tau$  rather by  $\tau$  itself

## Partial coherence

base transit time is a random variable with distribution **p** 

 $\alpha (\omega) = \int_{0}^{\omega} \rho (\tau) e^{-i\omega\tau} d\tau \qquad \text{characteristic}_{\text{function of } \rho}$ 

111



extended frequency cutoff

# Inclusion of collector transit







Coherent transistor has current gain at frequencies where the transistor which has no base delay at all is completely damped

What is special about base transit?

why is the phase gained in constant-velocity collector transit not as good as that gained in base transit ?

$$\alpha_{B} = e^{-i\phi}$$
$$\alpha_{c} = \frac{\sin\theta}{\theta} e^{-i\theta}$$





Coherent transistor loaded with parasitics



Example:  $C_X = 0$ 

$$U = \frac{\left|\alpha_{B}\alpha_{c}\right|^{2}}{4\omega^{2}C_{C}^{2}(R_{B}+R_{Bx})} \frac{1}{R_{\phi}+R_{X}}$$
$$R_{\phi} = \frac{\cos(\phi) - \cos(\phi+\theta)}{\omega C_{C} \theta} |\alpha_{B}|$$

 $\tau_x = R_x C_C$  where  $R_x$  combination of parasitic resistances





# Limitations

Ultra-high frequencies, cryogenic temperatures



# Coherence by other means



Coherence by Diffusion



to within cubic terms

 $\alpha_{B} = -\frac{1}{\cosh\left[2i\,\phi\right]^{1/2}}$ 

 $\phi << \pi$ 

 $e^{-\phi/3} e^{-i\phi}$ 



For large enough N gain phase  $\phi$  without sacrificing magnitude if N >  $\phi^2/3 \ln 2$ 

For  $\phi = \pi$  need N > 5

"Stepped up" diffusion



Essential condition: no return at the step Steps larger than optical phonon energy



 $f_T$  shifts by a factor of N due to enhancement of effective diffusion velocity

Resonant peak in current gain appears only for unrealistic values of N > 19

Exemplary AlGaAs HBT loaded with parasitics













$$\alpha = e^{-\phi^{2}/2r} e^{-i\phi}$$

where 
$$r = \frac{\tau_{diff}}{\tau_{drift}} = \frac{\Delta L G}{2 kT}$$

For N = 5 in AlGaAs need  $\Delta E_G \ge 180 \text{ meV}$ 

Same effect requires r = 7.5 need  $\Delta E_G$  = 15 kT = 380 meV

Consider W = 2000 A and D = 40 cm<sup>2</sup> / Vs Peak in U corresponding to the  $\phi = \pi$  resonance will occur at the frequency

f = 750 GHz

Summary

Anatoly A. Grinberg & SL IEEE TED 40, pp. 1512-1522 (August 93)

SL, AAG & Vera B. Gorfinkel APL 63, pp. 1537-1539 (September 13, 1993)

#### Coherent ballistic transistor

Nature of gain roll-off with frequency in collisionless base transport

It is possible to suppress Landau damping in a cryogenic HBT with abrupt junction

Phaseshift in coherent base propagation can be used to obtain active transistor behavior above conventional cutoff frequencies

Current gain at  $f >> f_T$ Power gain at  $f >> f_{max}$ Role of parasitics

Oscillation frequencies up to 1 THz are perhaps feasible at low temperatures; "slow" operation is not possible... very stringent requirements on the parasitics.

#### Coherent drift-diffusion transistor

It is possible to slow down and obtain coherent effects at room temperature in a graded-gap HBT

Step-base approach appears preferable

Reduction of the parasitic base-collector capacitance is necessary... Need: top collector HBT technology