

Junctions and Barriers

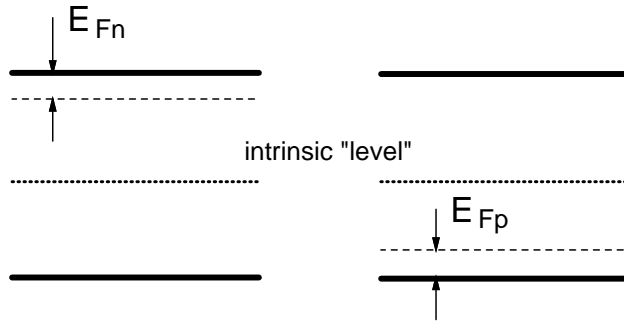


Figure 1: *n*- and *p*-type semiconductors

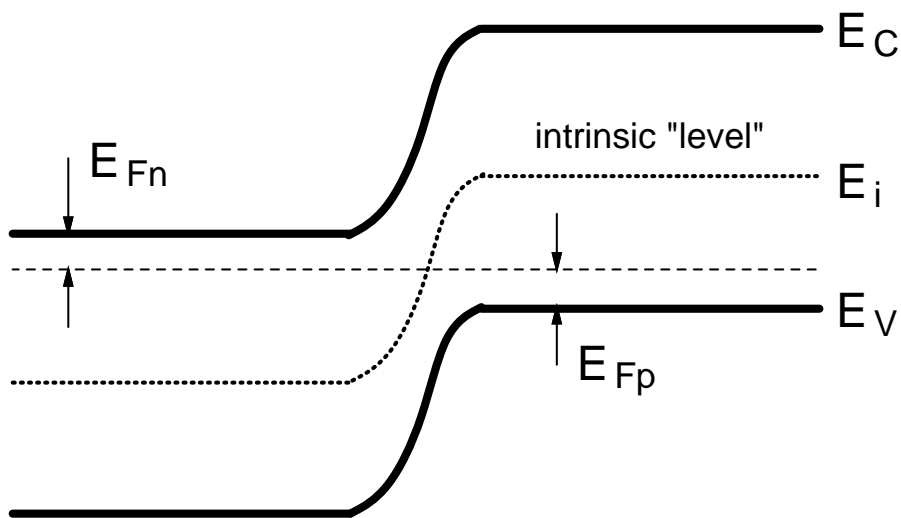


Figure 2: Metallurgical junction of two materials

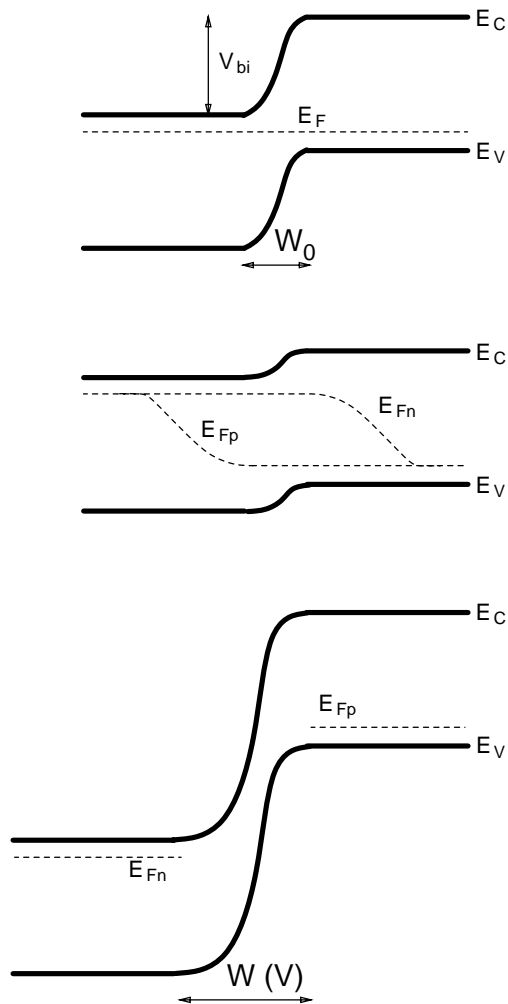


Figure 3: In forward bias, electrons flood the p -region and the holes flood the n region; this phenomenon is called **injection**. The region where the substantial amount of minority carriers reside is $0 < x < W$ determined by the **diffusion length** $L_D = \sqrt{D\tau}$.

In reverse bias, there are 2 components of the current (in the ideal pn junction): **generation** current ($\propto n_i \times W(V)$) and **diffusion** current [$\propto (n_i^2 / N_A) \times L_D / \tau$].

Diffusion. distance covered from home in time t on average is

$$L =$$

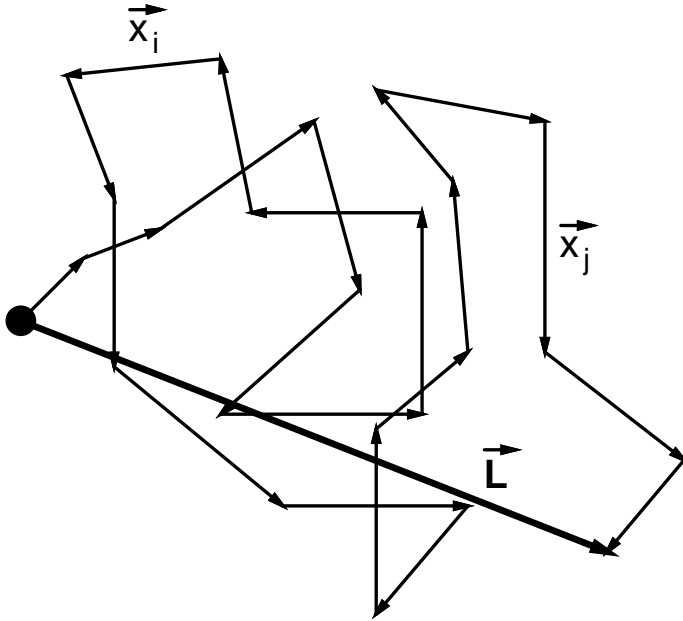


Figure 4: Random walk problem

$$\begin{aligned} \vec{L} &= \vec{x}_1 + \vec{x}_2 + \vec{x}_3 + \dots \\ L^2 &= \sum_{i,j} x_i x_j \\ \langle L^2 \rangle &= \sum_i \langle x_i^2 \rangle + \sum_{i \neq j} \langle x_i x_j \rangle = N \langle x^2 \rangle \\ \bar{L} &\equiv \bar{x} \end{aligned}$$

If $t = \tau$ is the lifetime, $L = L_D$ is called the diffusion length,

$$L_D =$$

L_D is a characteristic of the material. It is long (can be millimeters) in indirect materials (where τ is slow) and short in direct-gap III-V compounds (typically, several microns). L_D depends very much on traps, dislocations, grain boundaries, etc., which all affect primarily τ .

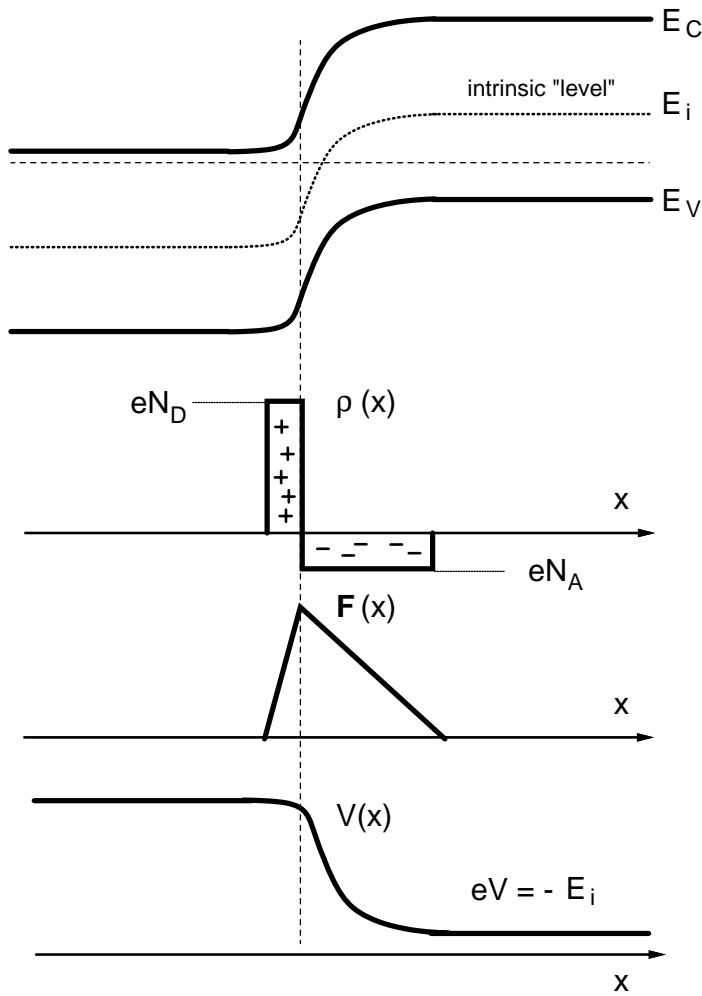


Figure 5: Charge, Field, and Potential profiles in a *pn*-junction

$$\frac{dF}{dx} = \frac{\rho}{\epsilon} \quad \text{Poisson}$$

$$\frac{dV}{dx} = -F \quad \text{definition of potential}$$

$$\frac{dE_C}{dx} = eF \quad \text{definition of potential energy}$$

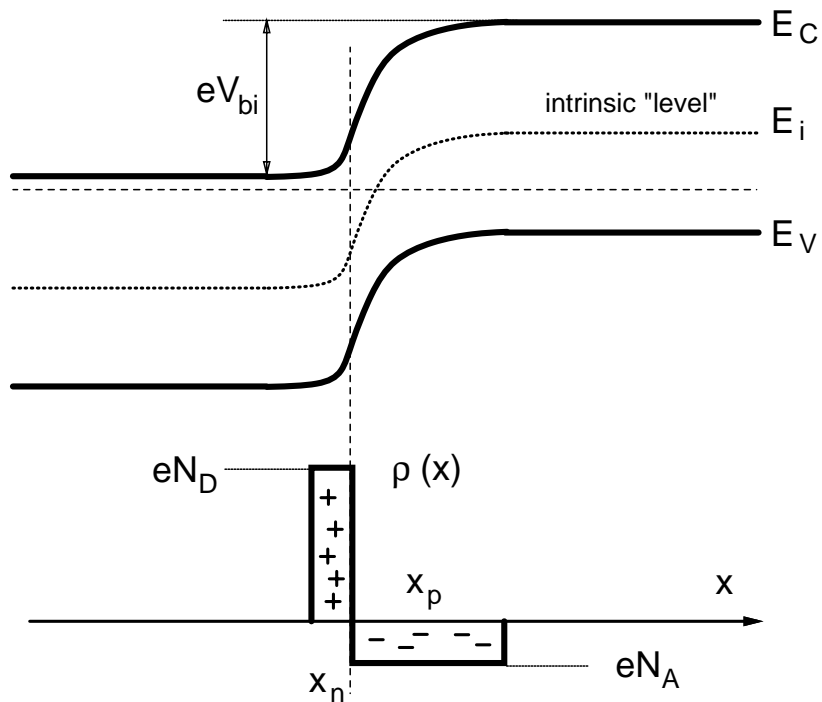


Figure 6: One-sided pn -junction ($N_D \gg N_A$)

Neutrality: $N_A x_p = N_D x_n$

Build-in voltage: $V_{bi} = kT \ln \frac{N_D N_A}{n_i^2}$

intrinsic concentration: $n_i^2 = N_C N_V e^{-E_g/kT}$

In equilibrium everywhere: $np = n_i^2$

To determine the electrostatic state of a *pn* junction
 (in equilibrium or under bias *V*)

1. Calculate or look up n_i
 [characteristic of the material, $n_i(T)$]

2. From N_D , N_A calculate V_{bi}

$$V_{bi} = kT \ln \frac{N_D N_A}{n_i^2}$$

3. Calculate x_n and x_p from

$$\frac{e N_D x_n^2}{2 \epsilon} + \frac{e N_A x_p^2}{2 \epsilon} = V_{bi} \pm V \quad (\text{from Poisson's eq.})$$

$$N_D x_n = N_A x_p \quad \text{neutrality}$$

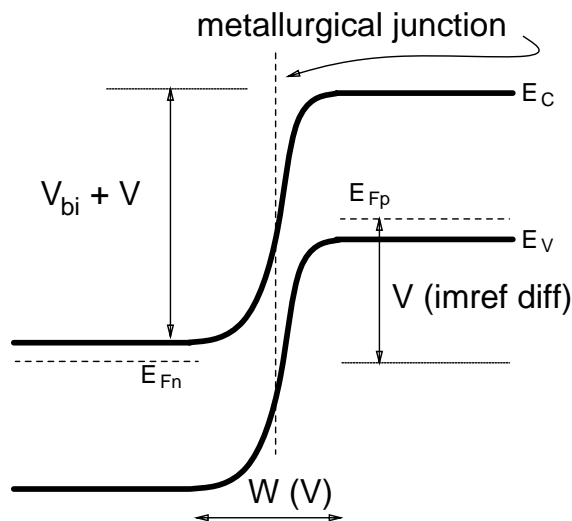
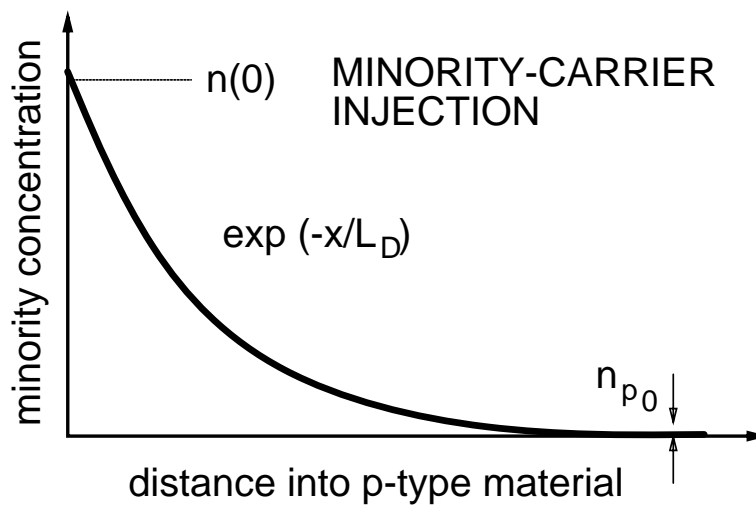
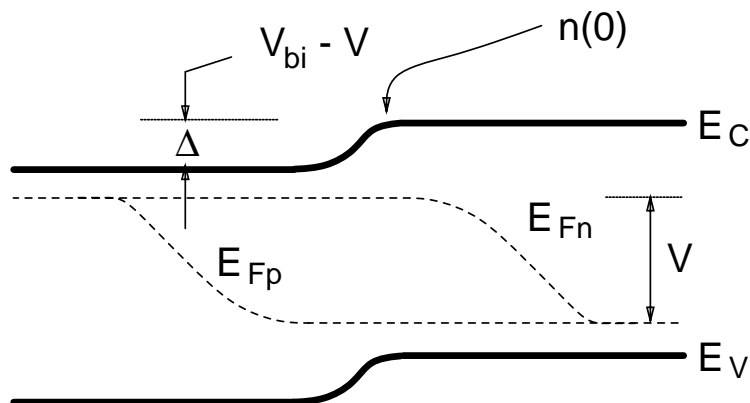


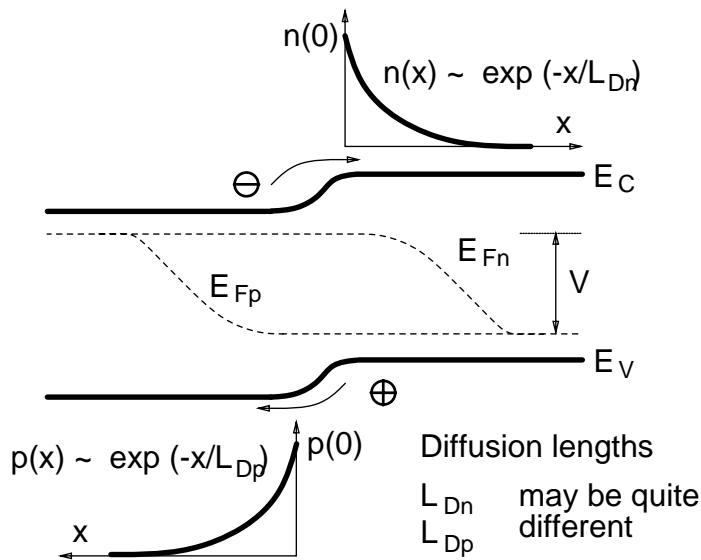
Figure 7: Reverse-biased *pn*-junction

pn junction under forward bias

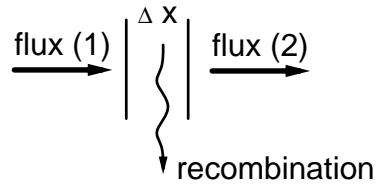
$$n(0) = n_{p0} e^{V/kT} \quad \text{where } n_{p0} \approx \frac{n_i^2}{N_A}$$

$$n(0) = n_{n0} e^{-\Delta/kT} \quad \text{where } n_{n0} \approx N_D$$





CONTINUITY EQUATION



$$\frac{d(n \Delta x)}{dt} = D \frac{dn(1)}{dx} - D \frac{dn(2)}{dx} - \frac{n - n_{p0}}{\tau} \Delta x$$

$$\Rightarrow D \frac{d^2 n}{dx^2} = \frac{n}{\tau} \quad (\text{in steady state})$$

$$n'' = \frac{n}{L_D^2} \quad \text{where } L_D \equiv$$

Solutions: $e^{\pm x/L_D}$ (in general, $A e^{x/L_D} + B e^{-x/L_D}$)

Physical choice: $(n - n_{p0}) = [n(0) - n_{p0}] e^{-x/L_D}$

Electron Diffusion Current:

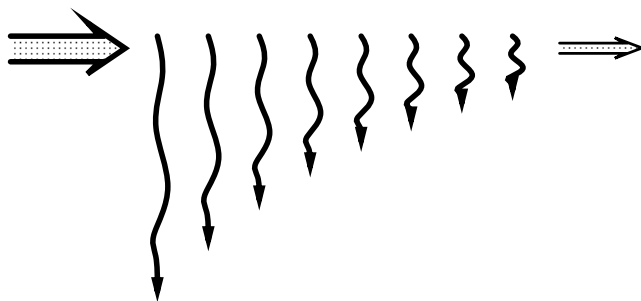
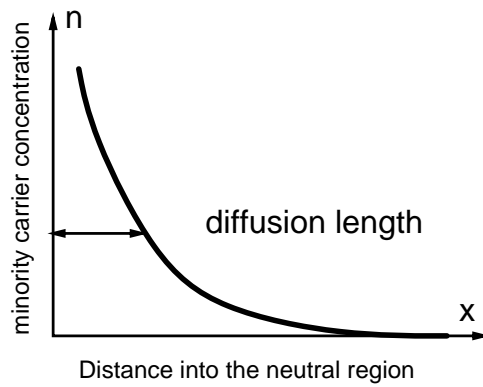
$$\begin{aligned}
 n - n_{p0} &= [n(0) - n_{p0}] e^{-x/L_D} \\
 e D \frac{dn}{dx} &= \frac{e D}{L_D} [n(0) - n_{p0}] e^{-x/L_D} \\
 &= e n_{p0} [e^{eV/kT} - 1] e^{-x/L_D}
 \end{aligned}$$

Conservation of charge:

$$\frac{d\rho}{dt} = 0 \quad \Rightarrow \quad \vec{\nabla} \cdot \vec{J} = 0$$

Hence

$$J_n + J_p = \text{Const} \quad (\text{independent of } x)$$



Affinity and Work Function

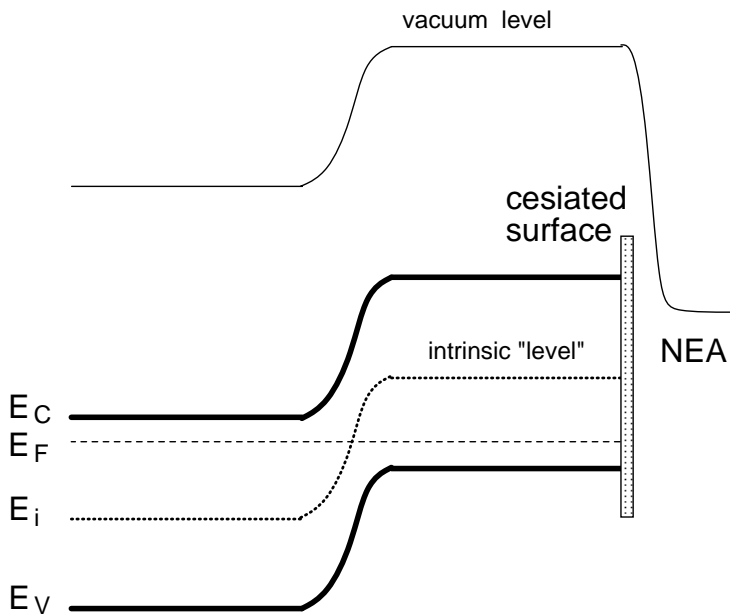
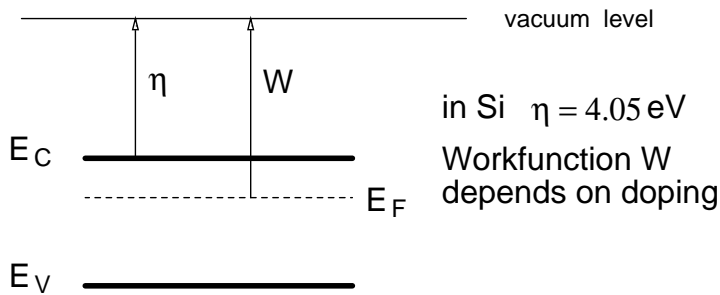


Figure 10: Illustration of electron affinity and the workfunction. Certain surface treatments (e.g., Cs or Cs oxide) produce surfaces with workfunctions as low as 1 or 2 eV. For a sufficiently widegap *p*-type semiconductor, this may result in a *negative affinity* (NEA) of the material near the surface.

Such a surface works as an "electron gun" or "cold cathode", shooting out any minority electrons injected in the region near the surface. Minority electrons can be injected by forward-biasing the *pn* junction.

Junction Capacitance

$$\frac{C}{A} = \frac{\epsilon}{W} = \frac{\epsilon}{x_n + x_p}$$

Exercise: small-signal derivation *Check that the same result is obtained!*

$$\frac{C}{A} = \frac{\delta Q}{\delta V}$$

where

$$\delta Q = e \delta x_n N_D = e \delta x_p N_A$$

