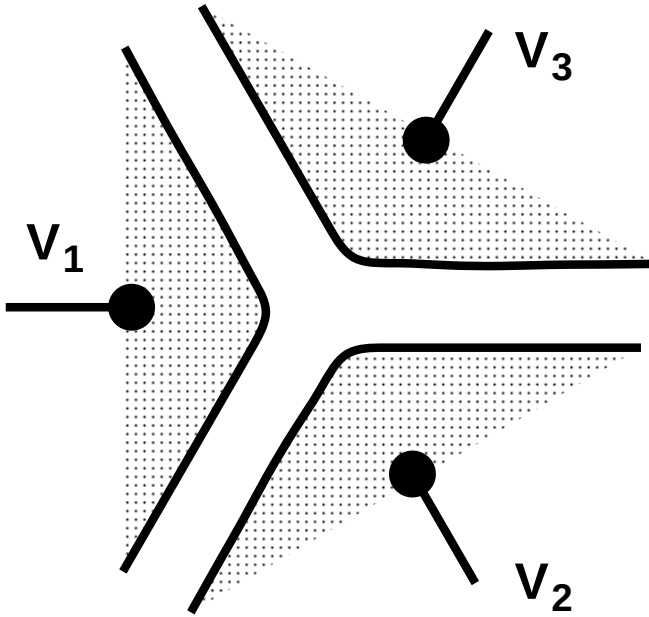

logic input	V_3	0				1			
	V_2	1	0	0	1	1	0	0	1
	V_1	0	0	1	1	0	0	1	1
output	I_C	1	0	1	1	1	1	1	0
function		or				nand			